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FIG. 1

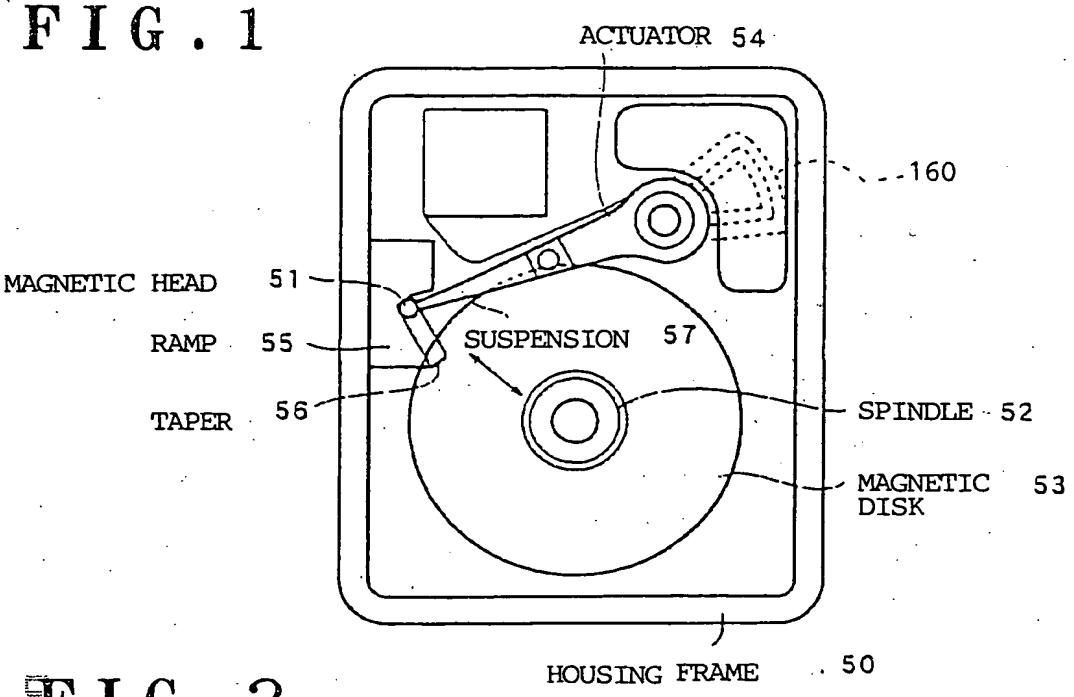

FIG. 2

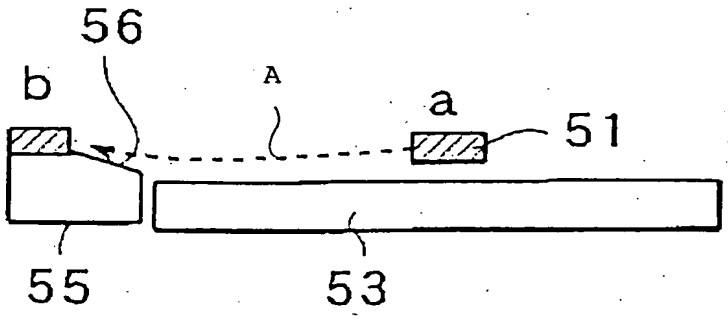

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FIG. 3

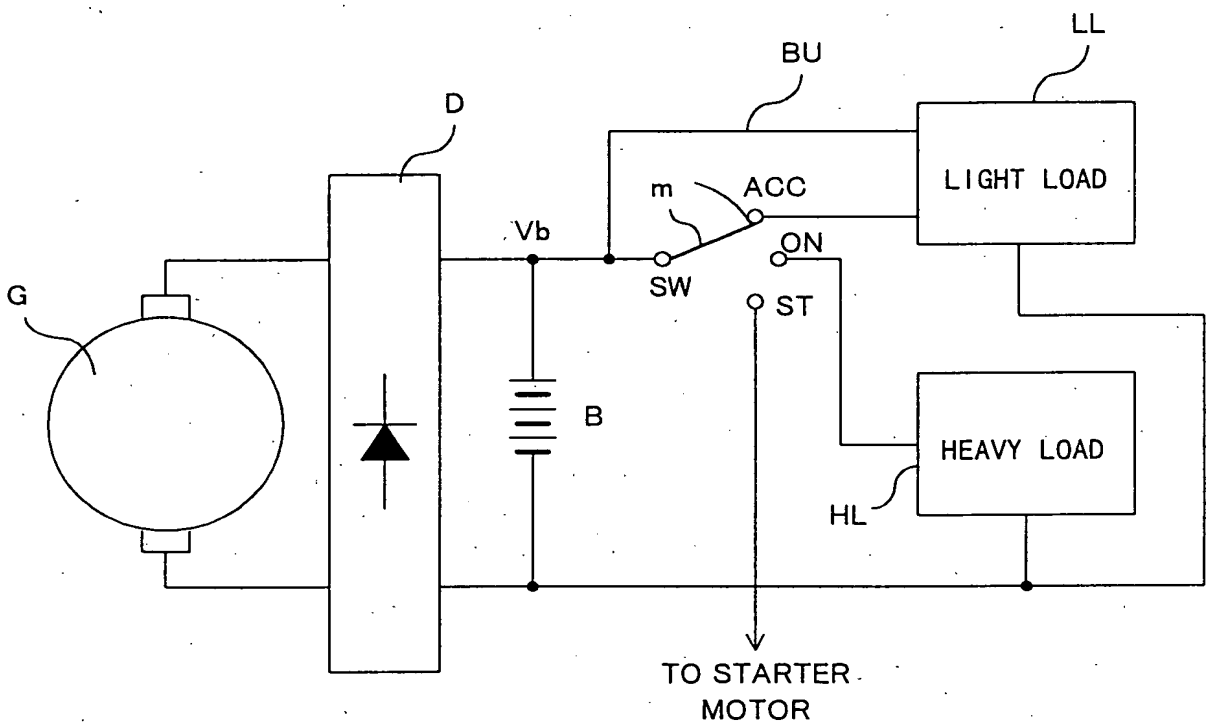

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FIG. 4A

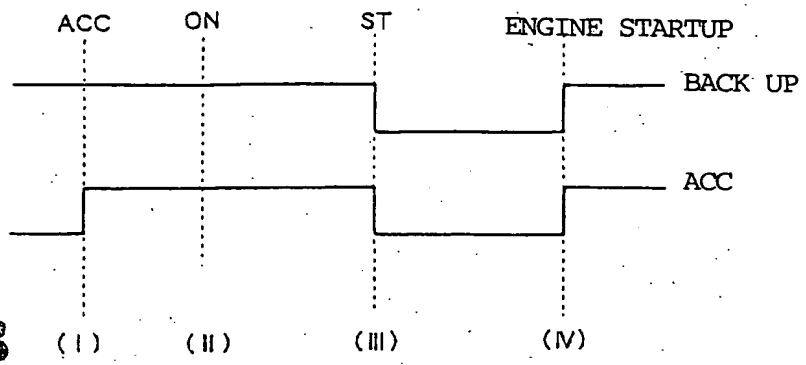

FIG. 4B

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FIG. 5

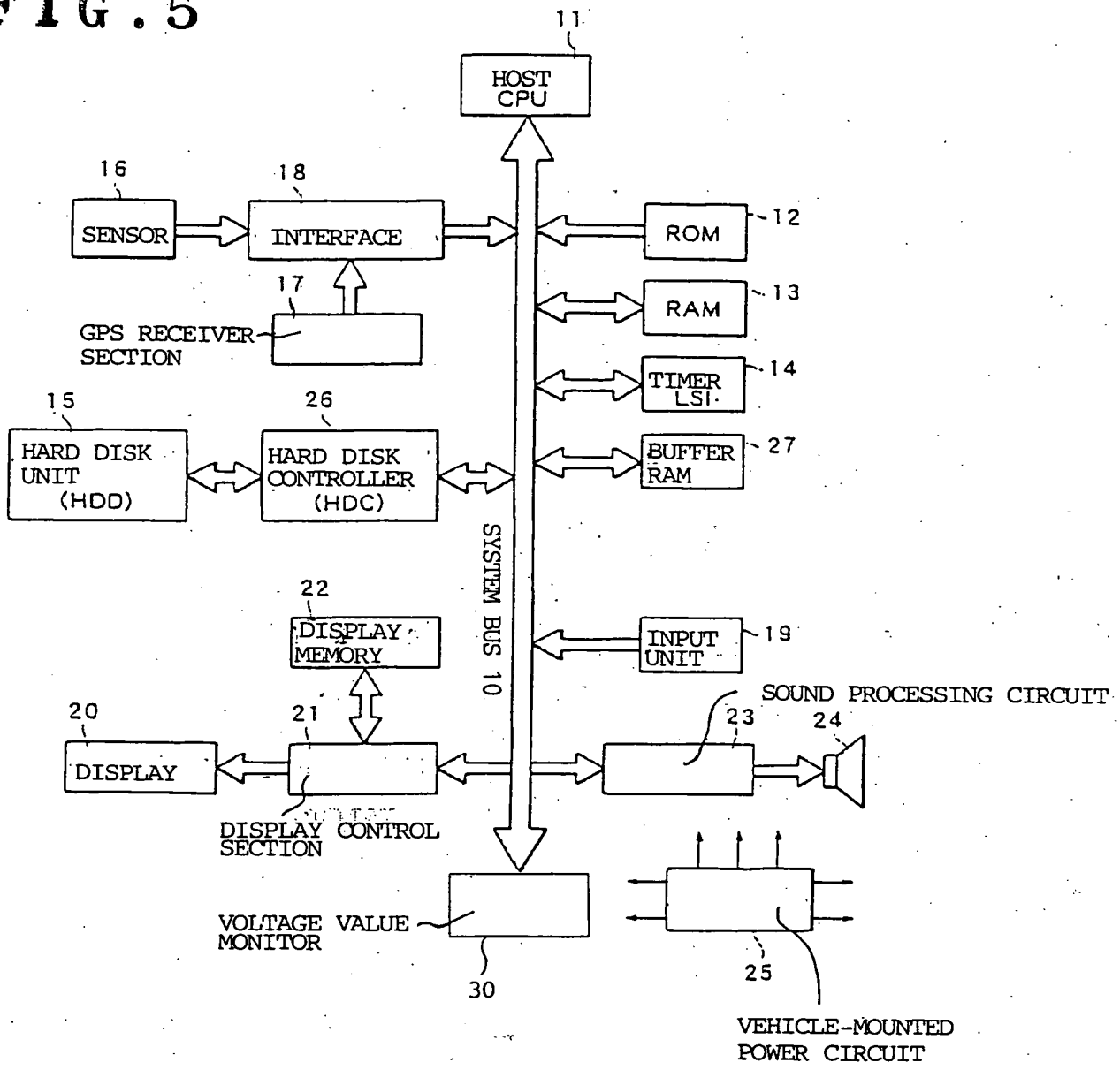

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FIG. 6

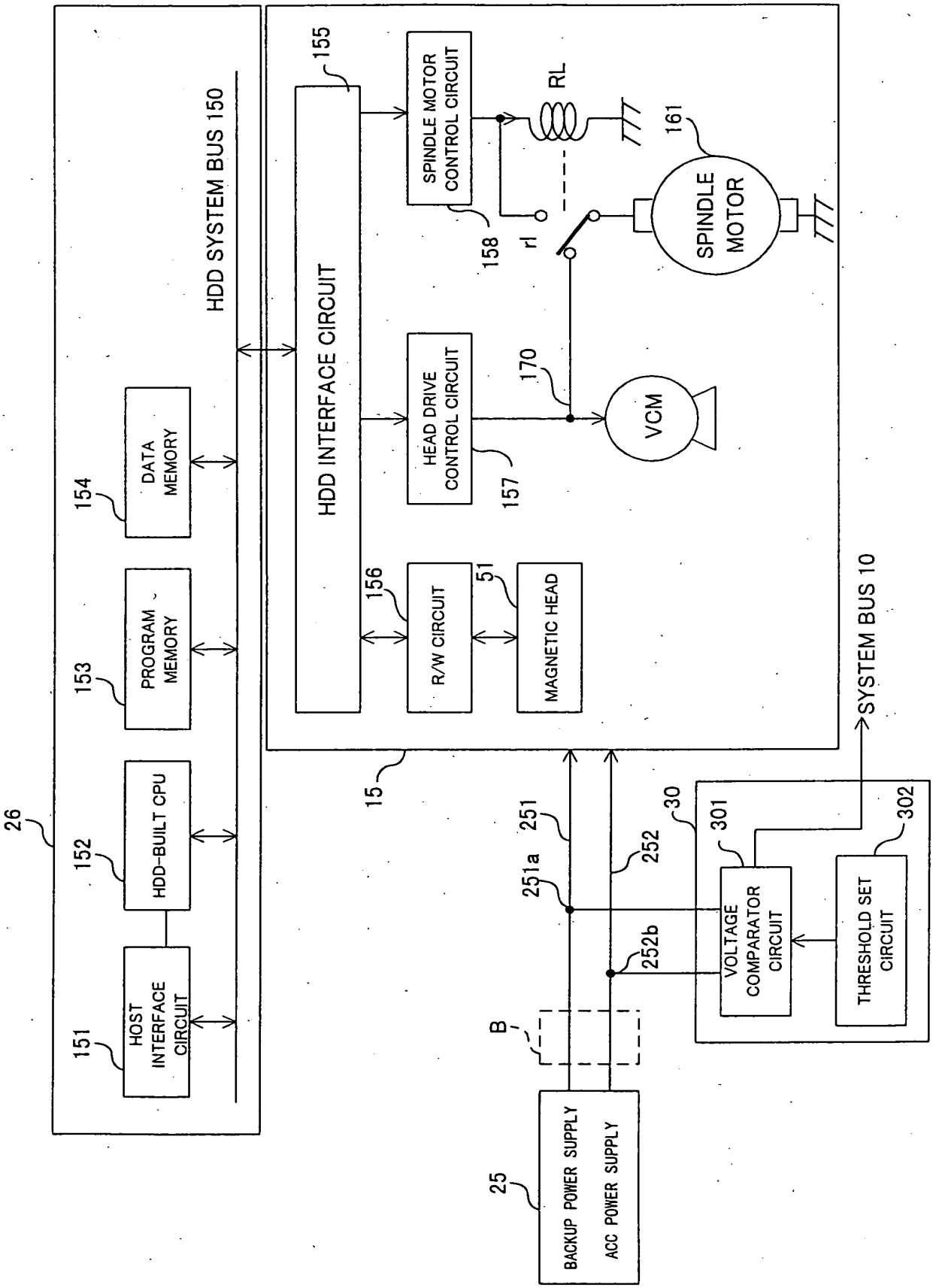
26
HDD SYSTEM BUS 150
151
HOST INTERFACE CIRCUIT
152
HDD-BUILT CPU
153
PROGRAM MEMORY
154
DATA MEMORY
15
HDD INTERFACE CIRCUIT
155
SPINDLE MOTOR CONTROL CIRCUIT
158
RL
r1
161
SPINDLE MOTOR
HEAD DRIVE CONTROL CIRCUIT
157
170
VCM
156
R/W CIRCUIT
51
MAGNETIC HEAD
SYSTEM BUS 10
251
251a
252
252b
30
301
302
VOLTAGE COMPARATOR CIRCUIT
THRESHOLD SET CIRCUIT
B
25
BACKUP POWER SUPPLY
ACC POWER SUPPLY

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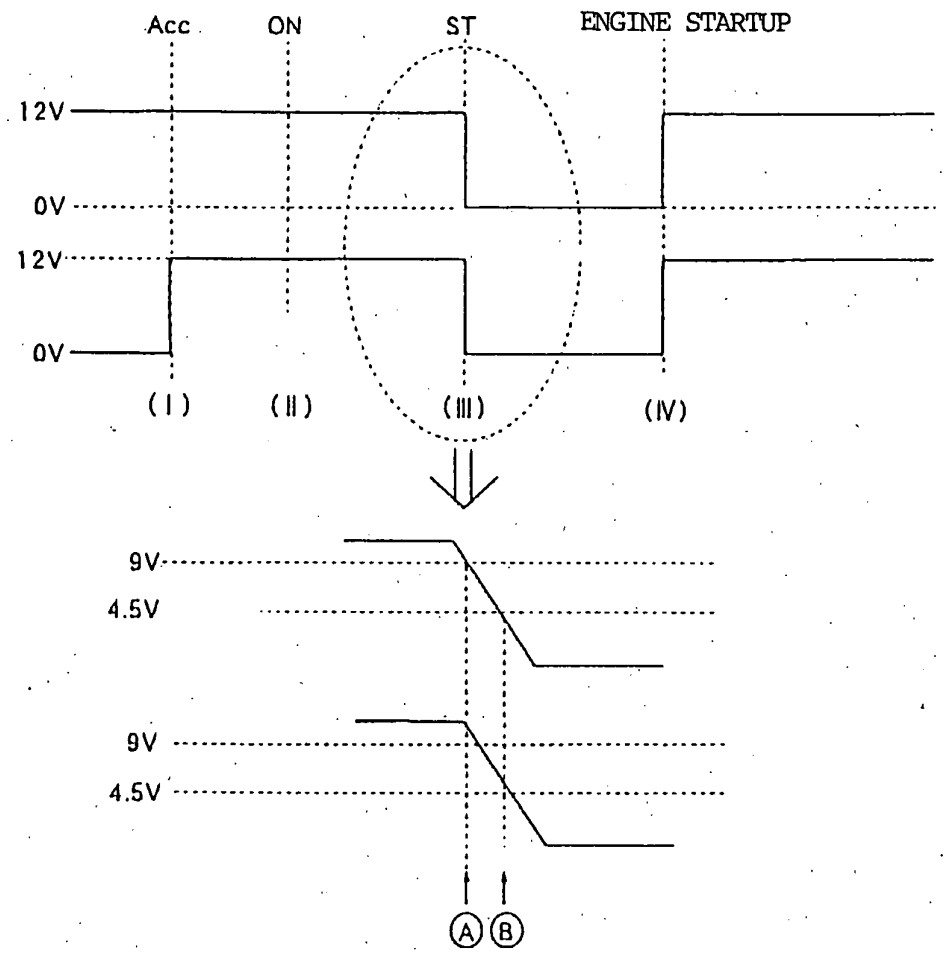
FIG.7A

FIG.7B

FIG.7C

FIG.7D



BACK UP

ACC

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FIG. 8A

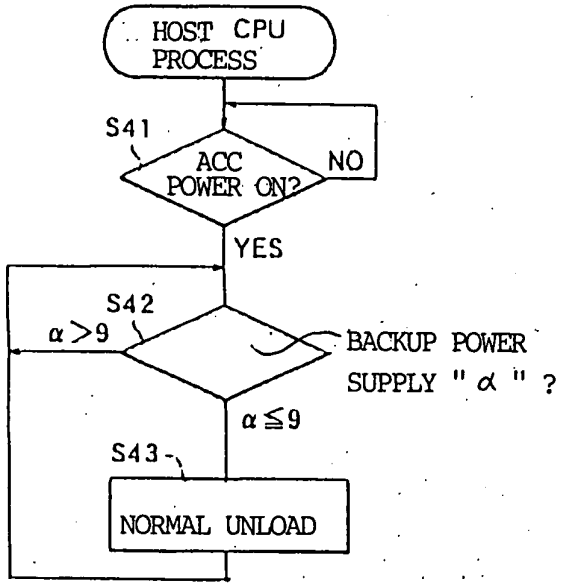

FIG. 8B

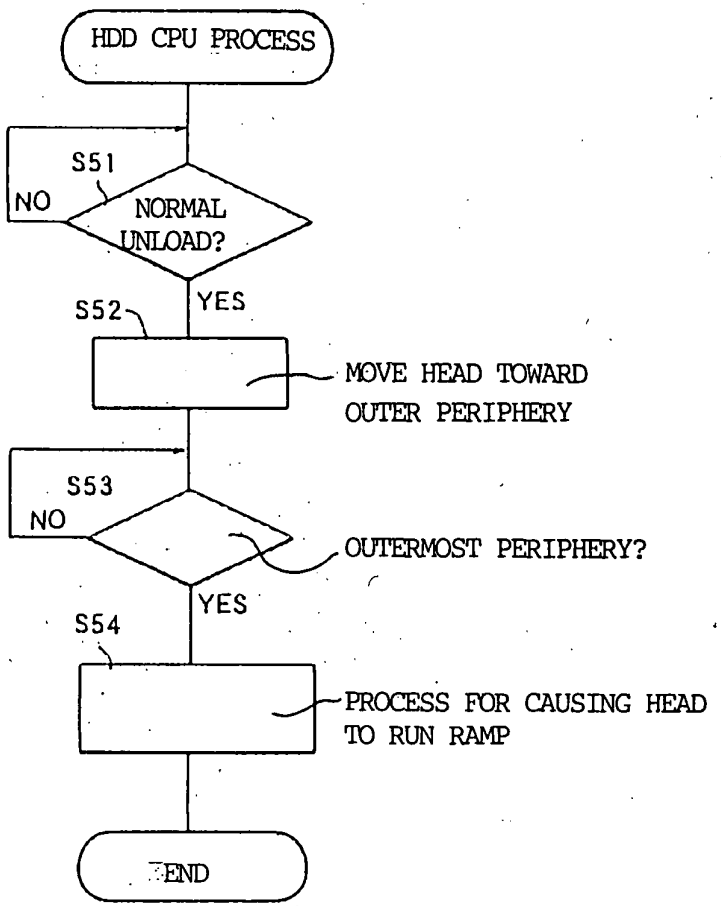

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FIG. 9A

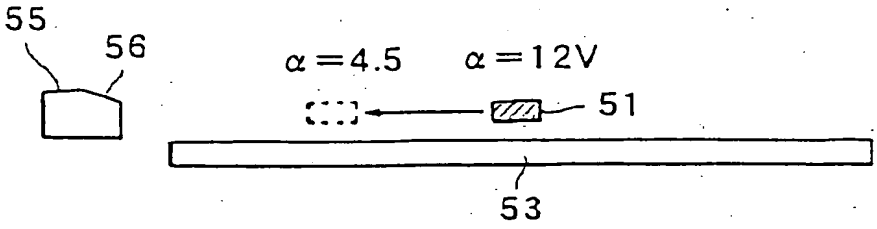
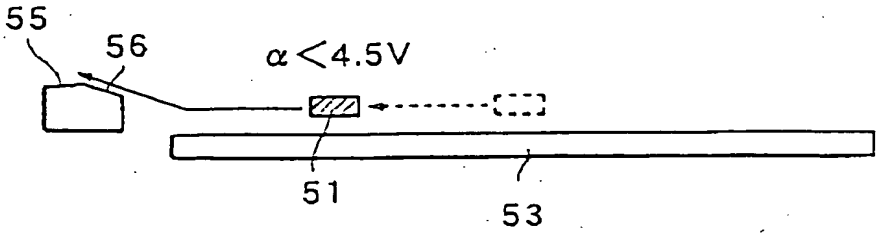


FIG. 9B



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